

Electronic Acknowledgement Receipt

EFS ID:	5056793
Application Number:	10811835
International Application Number:	
Confirmation Number:	3051
Title of Invention:	Method of designing a circuit layout of a semiconductor device
First Named Inventor/Applicant Name:	Kazuaki Goto
Customer Number:	78198
Filer:	Donald R. Studebaker
Filer Authorized By:	
Attorney Docket Number:	030712-29
Receipt Date:	30-MAR-2009
Filing Date:	30-MAR-2004
Time Stamp:	13:01:29
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$1030
RAM confirmation Number	10619
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Appeal Brief Filed	appealbrief3-30-09.pdf	805632 8856a91498578d307d99b74d2755cc2802756608	no	12
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	31858 9018720469e78c2152ae454dc7c36a8d1f2c8a1	no	2
Warnings:					
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Total Files Size (in bytes):			837490		
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